

**JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD**  
**B.Tech. in ELECTRONICS AND COMMUNICATION ENGINEERING**  
**COURSE STRUCTURE & SYLLABUS (R18)**

**Applicable From 2018-19 Admitted Batch**

**I YEAR I SEMESTER**

S. No.	Course Code	Course Title	L	T	P	Credits
1	MA101BS	Mathematics - I	3	1	0	4
2	AP102BS	Applied Physics	3	1	0	4
3	CS103ES	Programming for Problem Solving	3	1	0	4
4	ME104ES	Engineering Graphics	1	0	4	3
5	AP105BS	Applied Physics Lab	0	0	3	1.5
6	CS106ES	Programming for Problem Solving Lab	0	0	3	1.5
7	*MC109ES	Environmental Science	3	0	0	0
		Induction Programme				
		<b>Total Credits</b>	<b>13</b>	<b>3</b>	<b>10</b>	<b>18</b>

**I YEAR II SEMESTER**

S. No.	Course Code	Course Title	L	T	P	Credits
1	MA201BS	Mathematics - II	3	1	0	4
2	CH202BS	Chemistry	3	1	0	4
3	EE203ES	Basic Electrical Engineering	3	0	0	3
4	ME205ES	Engineering Workshop	1	0	3	2.5
5	EN205HS	English	2	0	0	2
6	CH206BS	Engineering Chemistry Lab	0	0	3	1.5
7	EN207HS	English Language and Communication Skills Lab	0	0	2	1
8	EE208ES	Basic Electrical Engineering Lab	0	0	2	1
		<b>Total Credits</b>	<b>12</b>	<b>2</b>	<b>10</b>	<b>19</b>

**II YEAR I SEMESTER**

S. No.	Course Code	Course Title	L	T	P	Credits
1	EC301PC	Electronic Devices and Circuits	3	1	0	4
2	EC302PC	Network Analysis and Transmission Lines	3	0	0	3
3	EC303PC	Digital System Design	3	1	0	4
4	EC304PC	Signals and Systems	3	1	0	4
5	EC305ES	Probability Theory and Stochastic Processes	3	0	0	3
6	EC306PC	Electronic Devices and Circuits Lab	0	0	2	1
7	EC307PC	Digital System Design Lab	0	0	2	1
8	EC308ES	Basic Simulation Lab	0	0	2	1
9	*MC309	Constitution of India	3	0	0	0
		<b>Total Credits</b>	<b>18</b>	<b>3</b>	<b>6</b>	<b>21</b>

**II YEAR II SEMESTER**

S. No.	Course Code	Course Title	L	T	P	Credits
1	MA401BS	Laplace Transforms, Numerical Methods & Complex Variables	3	1	0	4
2	EC402PC	Electromagnetic Fields and Waves	3	0	0	3

**EC301PC: ELECTRONIC DEVICES AND CIRCUITS****B.Tech. II Year I Sem.**

L	T	P	C
3	1	0	4

**Course Objectives:**

- To introduce components such as diodes, BJTs and FETs.
- To know the applications of components.
- To know the switching characteristics of components
- To give understanding of various types of amplifier circuits

**Course Outcomes:** Upon completion of the Course, the students will be able to:

- Know the characteristics of various components.
- Understand the utilization of components.
- Understand the biasing techniques
- Design and analyze small signal amplifier circuits.

**UNIT - I****Diode and Applications:** Diode - Static and Dynamic resistances, Equivalent circuit, Load line analysis, Diffusion and Transition Capacitances, Diode Applications: Switch-Switching times.

Rectifier - Half Wave Rectifier, Full Wave Rectifier, Bridge Rectifier, Rectifiers with Capacitive and Inductive Filters, Clippers-Clipping at two independent levels, Clamper-Clamping Circuit Theorem, Clamping Operation, Types of Clampers.

**UNIT - II****Bipolar Junction Transistor (BJT):** Principle of Operation, Common Emitter, Common Base and Common Collector Configurations, Transistor as a switch, switching times, Transistor Biasing and Stabilization - Operating point, DC & AC load lines, Biasing - Fixed Bias, Self Bias, Bias Stability, Bias Compensation using Diodes.**UNIT - III****Junction Field Effect Transistor (FET):** Construction, Principle of Operation, Pinch-Off Voltage, Volt-Ampere Characteristic, Comparison of BJT and FET, Biasing of FET, FET as Voltage Variable Resistor.**Special Purpose Devices:** Zener Diode - Characteristics, Voltage Regulator. Principle of Operation - SCR, Tunnel diode, UJT, Varactor Diode.**UNIT - IV****Analysis and Design of Small Signal Low Frequency BJT Amplifiers:** Transistor Hybrid model, Determination of h-parameters from transistor characteristics, Typical values of h- parameters in CE, CB and CC configurations, Transistor amplifying action, Analysis of CE, CC, CB Amplifiers and CE Amplifier with emitter resistance, low frequency response of BJT Amplifiers, effect of coupling and bypass capacitors on CE Amplifier.**UNIT - V****FET Amplifiers:** Small Signal Model, Analysis of JFET Amplifiers, Analysis of CS, CD, CG JFET Amplifiers. MOSFET Characteristics in Enhancement and Depletion mode, Basic Concepts of MOS Amplifiers.**TEXT BOOKS:**

1. Electronic Devices and Circuits- Jacob Millman, McGraw Hill Education
2. Electronic Devices and Circuits theory- Robert L. Boylestead, Louis Nashelsky, 11<sup>th</sup> Edition, 2009, Pearson.

**REFERENCE BOOKS:**

1. The Art of Electronics, Horowitz, 3<sup>rd</sup> Edition Cambridge University Press
2. Electronic Devices and Circuits, David A. Bell – 5<sup>th</sup> Edition, Oxford.
3. Pulse, Digital and Switching Waveforms –J. Millman, H. Taub and Mothiki S. Prakash Rao, 2Ed., 2008, Mc Graw Hill.

**EC302PC: NETWORK ANALYSIS AND TRANSMISSION LINES****B.Tech. II Year I Sem.**

L	T	P	C
3	0	0	3

**Pre-Requisites:** Nil**Course Objectives:**

- To understand the basic concepts on RLC circuits.
- To know the behavior of the steady states and transients states in RLC circuits.
- To understand the two port network parameters.
- To study the propagation, reflection and transmission of plane waves in bounded and unbounded media.

**Course Outcomes:** Upon successful completion of the course, students will be able to:

- Gain the knowledge on basic RLC circuits behavior.
- Analyze the Steady state and transient analysis of RLC Circuits.
- Know the characteristics of two port network parameters.
- Analyze the transmission line parameters and configurations.

**UNIT - I**

Network Topology, Basic cutset and tie set matrices for planar networks, Magnetic Circuits, Self and Mutual inductances, dot convention, impedance, reactance concept, Impedance transformation and coupled circuits, co-efficient of coupling, equivalent T for Magnetically coupled circuits, Ideal Transformer.

**UNIT - II**

Transient and Steady state analysis of RC, RL and RLC Circuits, Sinusoidal, Step and Square responses. RC Circuits as integrator and differentiators. 2<sup>nd</sup> order series and parallel RLC Circuits, Root locus, damping factor, over damped, under damped, critically damped cases, quality factor and bandwidth for series and parallel resonance, resonance curves.

**UNIT - III**

Two port network parameters, Z, Y, ABCD, h and g parameters, Characteristic impedance, Image transfer constant, image and iterative impedance, network function, driving point and transfer functions – using transformed (S) variables, Poles and Zeros. Standard T,  $\pi$ , L Sections, Characteristic impedance, image transfer constants, Design of Attenuators, impedance matching network.

**UNIT – IV**

Transmission Lines - I: Types, Parameters, Transmission Line Equations, Primary & Secondary Constants, Equivalent Circuit, Characteristic Impedance, Propagation Constant, Phase and Group Velocities, Infinite Line Concepts, Lossless / Low Loss Characterization, Types of Distortion, Condition for Distortion less line, Minimum Attenuation, Loading - Types of Loading.

**UNIT – V**

Transmission Lines – II: Input Impedance Relations, SC and OC Lines, Reflection Coefficient, VSWR.  $\lambda/4$ ,  $\lambda/2$ ,  $\lambda/8$  Lines – Impedance Transformations, Smith Chart – Configuration and Applications, Single Stub Matching.

**TEXT BOOKS:**

1. Network Analysis – Van Valkenburg, 3<sup>rd</sup> Ed., Pearson, 2016.
2. Networks, Lines and Fields - JD Ryder, PHI, 2<sup>nd</sup> Edition, 1999.

**REFERENCE BOOKS:**

1. Electric Circuits – J. Edminister and M. Nahvi – Schaum's Outlines, Mc Graw Hills Education, 1999.
2. Engineering Circuit Analysis – William Hayt and Jack E Kemmerly, MGH, 8<sup>th</sup> Edition, 1993.
3. Electromagnetics with Applications – JD. Kraus, 5<sup>th</sup> Ed., TMH
4. Transmission Lines and Networks – Umesh Sinha, Satya Prakashan, 2001, (Tech. India Publications), New Delhi.

**EC303PC: DIGITAL SYSTEM DESIGN****B.Tech. II Year I Sem.**

L	T	P	C
3	1	0	4

**Pre-Requisites:** Nil**Course Objectives:**

- To understand common forms of number representation in logic circuits
- To learn basic techniques for the design of digital circuits and fundamental concepts used in the design of digital systems.
- To understand the concepts of combinational logic circuits and sequential circuits.
- To understand the Realization of Logic Gates Using Diodes & Transistors.

**Course Outcomes:** Upon completing this course, the student will be able to

- Understand the numerical information in different forms and Boolean Algebra theorems
- Postulates of Boolean algebra and to minimize combinational functions
- Design and analyze combinational and sequential circuits
- Known about the logic families and realization of logic gates.

**UNIT - I:****Number Systems:** Number systems, Complements of Numbers, Codes- Weighted and Non-weighted codes and its Properties, Parity check code and Hamming code.**Boolean Algebra:** Basic Theorems and Properties, Switching Functions- Canonical and Standard Form, Algebraic Simplification, Digital Logic Gates, EX-OR gates, Universal Gates, Multilevel NAND/NOR realizations.**UNIT - II:****Minimization of Boolean functions:** Karnaugh Map Method - Up to five Variables, Don't Care Map Entries, Tabular Method,**Combinational Logic Circuits:** Adders, Subtractors, Comparators, Multiplexers, Demultiplexers, Encoders, Decoders and Code converters, Hazards and Hazard Free Relations.**UNIT - III****Sequential Circuits Fundamentals:** Basic Architectural Distinctions between Combinational and Sequential circuits, SR Latch, Flip Flops: SR, JK, JK Master Slave, D and T Type Flip Flops, Excitation Table of all Flip Flops, Timing and Triggering Consideration, Conversion from one type of Flip-Flop to another.**Registers and Counters:** Shift Registers – Left, Right and Bidirectional Shift Registers, Applications of Shift Registers - Design and Operation of Ring and Twisted Ring Counter, Operation of Asynchronous and Synchronous Counters.**UNIT - IV****Sequential Machines:** Finite State Machines, Synthesis of Synchronous Sequential Circuits- Serial Binary Adder, Sequence Detector, Parity-bit Generator, Synchronous Modulo N –Counters. Finite state machine-capabilities and limitations, Mealy and Moore models.**UNIT - V****Realization of Logic Gates Using Diodes & Transistors:** AND, OR and NOT Gates using Diodes and Transistors, DCTL, RTL, DTL, TTL, CML and CMOS Logic Families and its Comparison, Classification of Integrated circuits, comparison of various logic families, standard TTL NAND Gate-Analysis & characteristics, TTL open collector O/Ps, Tristate TTL, MOS & CMOS open drain and tri-state outputs, CMOS transmission gate, IC interfacing- TTL driving CMOS & CMOS driving TTL.**TEXT BOOKS:**

1. Switching and Finite Automata Theory - Zvi Kohavi & Niraj K. Jha, 3<sup>rd</sup> Edition, Cambridge, 2010.
2. Modern Digital Electronics – R. P. Jain, 3<sup>rd</sup> Edition, 2007- Tata McGraw-Hill

**REFERENCE BOOKS:**

1. Digital Design- Morris Mano, PHI, 4th Edition, 2006

2. Introduction to Switching Theory and Logic Design – Fredriac J. Hill, Gerald R. Peterson, 3rd Ed, John Wiley & Sons Inc.
3. Fundamentals of Logic Design- Charles H. Roth, Cengage Learning, 5th, Edition, 2004.
4. Switching Theory and Logic Design – A Anand Kumar, PHI, 2013

**EC304PC: SIGNALS AND SYSTEMS****B.Tech. II Year I Sem.**

L	T	P	C
3	1	0	4

**Pre-requisite:** Nil**Course Objectives:**

- This gives the basics of Signals and Systems required for all Electrical Engineering related courses.
- To understand the behavior of signal in time and frequency domain
- To understand the characteristics of LTI systems
- This gives concepts of Signals and Systems and its analysis using different transform techniques.

**Course Outcomes:** Upon completing this course, the student will be able to

- Differentiate various signal functions.
- Represent any arbitrary signal in time and frequency domain.
- Understand the characteristics of linear time invariant systems.
- Analyze the signals with different transform technique

**UNIT - I**

**Signal Analysis:** Analogy between Vectors and Signals, Orthogonal Signal Space, Signal approximation using Orthogonal functions, Mean Square Error, Closed or complete set of Orthogonal functions, Orthogonality in Complex functions, Classification of Signals and systems, Exponential and Sinusoidal signals, Concepts of Impulse function, Unit Step function, Signum function.

**UNIT – II**

**Fourier series:** Representation of Fourier series, Continuous time periodic signals, Properties of Fourier Series, Dirichlet's conditions, Trigonometric Fourier Series and Exponential Fourier Series, Complex Fourier spectrum.

**Fourier Transforms:** Deriving Fourier Transform from Fourier series, Fourier Transform of arbitrary signal, Fourier Transform of standard signals, Fourier Transform of Periodic Signals, Properties of Fourier Transform, Fourier Transforms involving Impulse function and Signum function, Introduction to Hilbert Transform.

**UNIT - III**

**Signal Transmission through Linear Systems:** Linear System, Impulse response, Response of a Linear System, Linear Time Invariant(LTI) System, Linear Time Variant (LTV) System, Transfer function of a LTI System, Filter characteristic of Linear System, Distortion less transmission through a system, Signal bandwidth, System Bandwidth, Ideal LPF, HPF, and BPF characteristics, Causality and Paley-Wiener criterion for physical realization, Relationship between Bandwidth and rise time, Convolution and Correlation of Signals, Concept of convolution in Time domain and Frequency domain, Graphical representation of Convolution.

**UNIT – IV**

**Laplace Transforms:** Laplace Transforms (L.T), Inverse Laplace Transform, Concept of Region of Convergence (ROC) for Laplace Transforms, Properties of L.T, Relation between L.T and F.T of a signal, Laplace Transform of certain signals using waveform synthesis.

**Z-Transforms:** Concept of Z- Transform of a Discrete Sequence, Distinction between Laplace, Fourier and Z Transforms, Region of Convergence in Z-Transform, Constraints on ROC for various classes of signals, Inverse Z-transform, Properties of Z-transforms.

**UNIT - V**

**Sampling theorem:** Graphical and analytical proof for Band Limited Signals, Impulse Sampling, Natural and Flat top Sampling, Reconstruction of signal from its samples, Effect of under sampling – Aliasing, Introduction to Band Pass Sampling.

**Correlation:** Cross Correlation and Auto Correlation of Functions, Properties of Correlation Functions, Energy Density Spectrum, Parseval's Theorem, Power Density Spectrum, Relation between Autocorrelation Function and Energy/Power Spectral Density Function, Relation between Convolution

and Correlation, Detection of Periodic Signals in the presence of Noise by Correlation, Extraction of Signal from Noise by Filtering.

**TEXT BOOKS:**

1. Signals, Systems & Communications - B.P. Lathi, 2013, BSP.
2. Signals and Systems - A.V. Oppenheim, A.S. Willsky and S.H. Nawabi, 2 Ed.

**REFERENCE BOOKS:**

1. Signals and Systems – Simon Haykin and Van Veen, Wiley 2 Ed.,
2. Signals and Systems – A. Rama Krishna Rao, 2008, TMH
3. Fundamentals of Signals and Systems - Michel J. Robert, 2008, MGH International Edition.
4. Signals, Systems and Transforms - C. L. Philips, J.M.Parr and Eve A.Riskin, 3 Ed., 2004, PE.
5. Signals and Systems – K. Deergha Rao, Birkhauser, 2018.

**EC305ES: PROBABILITY THEORY AND STOCHASTIC PROCESSES**

B.Tech. II Year I Sem.

L	T	P	C
3	0	0	3

Pre-requisite: Nil

**Course Objectives:**

- This gives basic understanding of random signals and processes
- Utilization of Random signals and systems in Communications and Signal Processing areas.
- To know the Spectral and temporal characteristics of Random Process.
- To Learn the Basic concepts of Noise sources

**Course Outcomes:** Upon completing this course, the student will be able to

- Understand the concepts of Random Process and its Characteristics.
- Understand the response of linear time Invariant system for a Random Processes.
- Determine the Spectral and temporal characteristics of Random Signals.
- Understand the concepts of Noise in Communication systems.

**UNIT - I**

**Probability & Random Variable:** Probability introduced through Sets and Relative Frequency: Experiments and Sample Spaces, Discrete and Continuous Sample Spaces, Events, Probability Definitions and Axioms, Joint Probability, Conditional Probability, Total Probability, Bay's Theorem, Independent Events, *Random Variable*- Definition, Conditions for a Function to be a Random Variable, Discrete, Continuous and Mixed Random Variable, Distribution and Density functions, Properties, Binomial, Poisson, Uniform, Gaussian, Exponential, Rayleigh, Methods of defining Conditioning Event, Conditional Distribution, Conditional Density and their Properties.

**UNIT - II**

**Operations on Single & Multiple Random Variables – Expectations:** Expected Value of a Random Variable, Function of a Random Variable, Moments about the Origin, Central Moments, Variance and Skew, Chebychev's Inequality, Characteristic Function, Moment Generating Function, Transformations of a Random Variable: Monotonic and Non-monotonic Transformations of Continuous Random Variable, Transformation of a Discrete Random Variable.

Vector Random Variables, Joint Distribution Function and its Properties, Marginal Distribution Functions, Conditional Distribution and Density – Point Conditioning, Conditional Distribution and Density – Interval conditioning, Statistical Independence.

Sum of Two Random Variables, Sum of Several Random Variables, Central Limit Theorem, (Proof not expected). Unequal Distribution, Equal Distributions. Expected Value of a Function of Random Variables: Joint Moments about the Origin, Joint Central Moments, Joint Characteristic Functions, Jointly Gaussian Random Variables: Two Random Variables case, N Random Variable case, Properties, Transformations of Multiple Random Variables, Linear Transformations of Gaussian Random Variables.

**UNIT - III**

**Random Processes – Temporal Characteristics:** The Random Process Concept, Classification of Processes, Deterministic and Nondeterministic Processes, Distribution and Density Functions, concept of Stationarity and Statistical Independence. First-Order Stationary Processes, Second- Order and Wide-Sense Stationarity, (N-Order) and Strict-Sense Stationarity, Time Averages and Ergodicity, Mean-Ergodic Processes, Correlation-Ergodic Processes, Autocorrelation Function and Its Properties, Cross-Correlation Function and Its Properties, Covariance Functions, Gaussian Random Processes, Poisson Random Process. Random Signal Response of Linear Systems: System Response – Convolution, Mean and Mean-squared Value of System Response, autocorrelation Function of Response, Cross-Correlation Functions of Input and Output.

**UNIT - IV**

**Random Processes – Spectral Characteristics:** The Power Spectrum: Properties, Relationship between Power Spectrum and Autocorrelation Function, The Cross-Power Density Spectrum, Properties, Relationship between Cross-Power Spectrum and Cross-Correlation Function. Spectral



Characteristics of System Response: Power Density Spectrum of Response, Cross-Power Density Spectrums of Input and Output.

**UNIT - V**

**Noise Sources & Information Theory:** Resistive/Thermal Noise Source, Arbitrary Noise Sources, Effective Noise Temperature, Noise equivalent bandwidth, Average Noise Figures, Average Noise Figure of cascaded networks, Narrow Band noise, Quadrature representation of narrow band noise & its properties. Entropy, Information rate, Source coding: Huffman coding, Shannon Fano coding, Mutual information, Channel capacity of discrete channel, Shannon-Hartley law; Trade -off between bandwidth and SNR.

**TEXT BOOKS:**

1. Probability, Random Variables & Random Signal Principles - Peyton Z. Peebles, TMH, 4<sup>th</sup> Edition, 2001.
2. Principles of Communication systems by Taub and Schilling (TMH),2008

**REFERENCE BOOKS:**

1. Random Processes for Engineers-Bruce Hajck, Cambridge unipress,2015
2. Probability, Random Variables and Stochastic Processes – Athanasios Papoulis and S. Unnikrishna Pillai, PHI, 4th Edition, 2002.
3. Probability, Statistics & Random Processes-K. Murugesan, P. Guruswamy, Anuradha Agencies, 3<sup>rd</sup> Edition, 2003.
4. Signals, Systems & Communications - B.P. Lathi, B.S. Publications, 2003.
5. Statistical Theory of Communication – S.P Eugene Xavier, New Age Publications, 2003

**EC306PC: ELECTRONIC DEVICES AND CIRCUITS LAB****B.Tech. II Year I Sem.**

L	T	P	C
0	0	2	1

**List of Experiments (Twelve experiments to be done):**

Verify any twelve experiments in H/W Laboratory

1. PN Junction diode characteristics A) Forward bias B) Reverse bias.
2. Zener diode characteristics and Zener as voltage Regulator
3. Full Wave Rectifier with & without filters
4. Input and output characteristics of BJT in CE Configuration
5. Input and output characteristics of FE in CS Configuration
6. Common Emitter Amplifier Characteristics
7. Common Base Amplifier Characteristics
8. Common Source amplifier Characteristics
9. Measurement of h-parameters of transistor in CB, CE, CC configurations
10. Switching characteristics of a transistor
11. SCR Characteristics.
12. Types of Clippers at different reference voltages
13. Types of Clampers at different reference voltages
14. The steady state output waveform of clampers for a square wave input

**Major Equipment required for Laboratories:**

1. Regulated Power Suppliers, 0-30V
2. 20 MHz, Dual Channel Cathode Ray Oscilloscopes.
3. Functions Generators-Sine and Square wave signals
4. Multimeters
5. Electronic Components

**EC307PC: DIGITAL SYSTEM DESIGN LAB****B.Tech. II Year I Sem.**

<b>L</b>	<b>T</b>	<b>P</b>	<b>C</b>
<b>0</b>	<b>0</b>	<b>2</b>	<b>1</b>

**Note: Implement using digital ICs, all experiments to be carried out.****List of Experiments**

1. Realization of Boolean Expressions using Gates
2. Design and realization logic gates using universal gates
3. Generation of clock using NAND / NOR gates
4. Design a 4 – bit Adder / Subtractor
5. Design and realization of a 4 – bit gray to Binary and Binary to Gray Converter
6. Design and realization of an 8 bit parallel load and serial out shift register using flip-flops.
7. Design and realization of a Synchronous and Asynchronous counter using flip-flops
8. Design and realization of Asynchronous counters using flip-flops
9. Design and realization of 8x1 MUX using 2x1 MUX
10. Design and realization of 4 bit comparator
11. Design and Realization of a sequence detector-a finite state machine

**Major Equipments required for Laboratories:**

1. 5 V Fixed Regulated Power Supply/ 0-5V or more Regulated Power Supply.
2. 20 MHz Oscilloscope with Dual Channel.
3. Bread board and components/ Trainer Kit.
4. Multimeter.

**EC308ES: BASIC SIMULATION LAB****B.Tech. II Year I Sem.**

L	T	P	C
0	0	2	1

**Note:**

- All the experiments are to be simulated using MATLAB or equivalent software
- Minimum of 15 experiment are to be completed

**List of Experiments:**

1. Basic Operations on Matrices.
2. Generation of Various Signals and Sequences (Periodic and Aperiodic), such as Unit Impulse, Unit Step, Square, Saw tooth, Triangular, Sinusoidal, Ramp, Sinc.
3. Operations on Signals and Sequences such as Addition, Multiplication, Scaling, Shifting, Folding, Computation of Energy and Average Power.
4. Finding the Even and Odd parts of Signal/Sequence and Real and Imaginary parts of Signal.
5. Convolution for Signals and sequences.
6. Auto Correlation and Cross Correlation for Signals and Sequences.
7. Verification of Linearity and Time Invariance Properties of a given Continuous/Discrete System.
8. Computation of Unit sample, Unit step and Sinusoidal responses of the given LTI system and verifying its physical realizability and stability properties.
9. Gibbs Phenomenon Simulation.
10. Finding the Fourier Transform of a given signal and plotting its magnitude and phase spectrum.
11. Waveform Synthesis using Laplace Transform.
12. Locating the Zeros and Poles and plotting the Pole-Zero maps in S-plane and Z-Plane for the given transfer function.
13. Generation of Gaussian noise ( Real and Complex), Computation of its mean, M.S. Value and its Skew, Kurtosis, and PSD, Probability Distribution Function.
14. Verification of Sampling Theorem.
15. Removal of noise by Autocorrelation / Cross correlation.
16. Extraction of Periodic Signal masked by noise using Correlation.
17. Verification of Weiner-Khinchine Relations.
18. Checking a Random Process for Stationarity in Wide sense.

**Major Equipments required for Laboratories:**

1. Computer System with latest specifications connected
2. Window Xp or equivalent
3. Simulation software-MAT Lab or any equivalent simulation software

**\*MC309/\*MC409: CONSTITUTION OF INDIA****B.Tech. II Year I Sem.**

L	T	P	C
3	0	0	0

The Constitution of India is the supreme law of India. Parliament of India cannot make any law which violates the Fundamental Rights enumerated under the Part III of the Constitution. The Parliament of India has been empowered to amend the Constitution under Article 368, however, it cannot use this power to change the “basic structure” of the constitution, which has been ruled and explained by the Supreme Court of India in its historical judgments. The Constitution of India reflects the idea of “Constitutionalism” – a modern and progressive concept historically developed by the thinkers of “liberalism” – an ideology which has been recognized as one of the most popular political ideology and result of historical struggles against arbitrary use of sovereign power by state. The historic revolutions in France, England, America and particularly European Renaissance and Reformation movement have resulted into progressive legal reforms in the form of “constitutionalism” in many countries. The Constitution of India was made by borrowing models and principles from many countries including United Kingdom and America.

The Constitution of India is not only a legal document but it also reflects social, political and economic perspectives of the Indian Society. It reflects India’s legacy of “diversity”. It has been said that Indian constitution reflects ideals of its freedom movement; however, few critics have argued that it does not truly incorporate our own ancient legal heritage and cultural values. No law can be “static” and therefore the Constitution of India has also been amended more than one hundred times. These amendments reflect political, social and economic developments since the year 1950. The Indian judiciary and particularly the Supreme Court of India has played an historic role as the guardian of people. It has been protecting not only basic ideals of the Constitution but also strengthened the same through progressive interpretations of the text of the Constitution. The judicial activism of the Supreme Court of India and its historic contributions has been recognized throughout the world and it gradually made it “as one of the strongest court in the world”.

**Course content**

1. Meaning of the constitution law and constitutionalism
2. Historical perspective of the Constitution of India
3. Salient features and characteristics of the Constitution of India
4. Scheme of the fundamental rights
5. The scheme of the Fundamental Duties and its legal status
6. The Directive Principles of State Policy – Its importance and implementation
7. Federal structure and distribution of legislative and financial powers between the Union and the States
8. Parliamentary Form of Government in India – The constitution powers and status of the President of India
9. Amendment of the Constitutional Powers and Procedure
10. The historical perspectives of the constitutional amendments in India
11. Emergency Provisions: National Emergency, President Rule, Financial Emergency
12. Local Self Government – Constitutional Scheme in India
13. Scheme of the Fundamental Right to Equality
14. Scheme of the Fundamental Right to certain Freedom under Article 19
15. Scope of the Right to Life and Personal Liberty under Article 21